## Features

- Pulse-width Modulation up to 2-kHz Clock Frequency
- Protection Against Short-circuit, Load-dump Overvoltage and Reverse $\mathbf{V}_{\mathbf{S}}$
- Duty Cycle 0\% to 100\% Continuously
- Output Stage for Power MOSFET
- Interference and Damage Protection According to VDE 0839 and ISO/TR 7637/1
- Charge-pump Noise Suppressed
- Ground-wire Breakage Protection


## 1. Description

The U6084B is a PWM-IC with bipolar technology designed for the control of an N -channel power MOSFET used as a high-side switch. The IC is ideal for use in the brightness control (dimming) of lamps such as in dashboard applications. For constant brightness, the preselected duty cycle can be reduced automatically as a function of the supply voltage.

Figure 1-1. Block Diagram with External Circuit


## 2. Pin Configuration

Figure 2-1. Pinning SO16


Table 2-1. Pin Description

| Pin | Symbol | Function |
| :---: | :---: | :--- |
| 1 | GND | IC ground |
| 2 | EN/DIS | Enable/disable |
| 3 | VI | Control input (duty cycle) |
| 4 | REDUCT | Duty cycle reduction |
| 5 | NC | Attenuation |
| 6 | OSC | Oscillator |
| 7 | NC | Not connected |
| 8 | NC | Not connected |
| 9 | LATCH | Status short-circuit latch |
| 10 | NC | Not connected |
| 11 | DELAY | Short-circuit protection delay |
| 12 | SENSE | Current sensing |
| 13 | 2 VS | Voltage doubler |
| 14 | OUTPUT | Output |
| 15 | NC | Not connected |
| 16 | VS | Supply voltage $\mathrm{V}_{\text {s }}$ |

## 3. Functional Description

### 3.1 Pin1 - GND

### 3.1.1 Ground-wire Breakage

To protect the FET in case of ground-wire breakage, a $820-\mathrm{k} \Omega$ resistor between gate and source is recommended to provide proper switch-off conditions.

### 3.2 Pin 2 - Enable/Disable

The dimmer can be switched on or off with pin 2 , independently of the set duty cycle.
Table 3-1. Pin 2 Function

| $\mathbf{V}_{\mathbf{2}}$ | Function |
| :--- | :--- |
| $>$ approximately 0.7 V or open | Disable |
| $<0.7 \mathrm{~V}$ or connected to pin 1 | Enable |

### 3.3 Pin 3 - Control Input

The pulse width is controlled by means of an external potentiometer ( $47 \mathrm{k} \Omega$ ). The characteristic (angle of rotation/duty cycle) is linear. The duty cycle be varied from $0 \%$ to $100 \%$. It is possible to further restrict the duty cycle with resistors $\mathrm{R}_{1}$ and $\mathrm{R}_{2}$ (Figure 7-1 on page 9).

Pin 3 is protected against short-circuit to $\mathrm{V}_{\text {Batt }}$ and ground GND ( $\mathrm{V}_{\text {Batt }} \leq 16.5 \mathrm{~V}$ ).

### 3.4 Pin 4 - Duty Cycle Reduction

With pin 4 connected according to Figure 7-1 on page 9, the set duty cycle is reduced to $\mathrm{V}_{\text {Batt }} \approx 12.5 \mathrm{~V}$. This causes a power reduction in the FET and in the lamps. In addition, the brightness of the lamps is largely independent of the supply voltage range, $\mathrm{V}_{\text {Batt }}=12.5 \mathrm{~V}$ to 16 V .

### 3.4.1 Output Slope Control

The rise and fall time ( $t_{r}, t_{f}$ ) of the lamp voltage can be limited to reduce radio interference. This is done with an integrator which controls a power MOSFET as source follower. The slope time is controlled by an external capacitor $\mathrm{C}_{4}$ and the oscillator current (Figure 7-1 on page 9).
Calculation:
$t_{f}=t_{r}=V_{\text {Batt }} \times \frac{C_{4}}{I_{\text {osc }}}$
With $\mathrm{V}_{\text {Batt }}=12 \mathrm{~V}, \mathrm{C}_{4}=470 \mathrm{pF}$ and $\mathrm{I}_{\text {osc }}=40 \mu \mathrm{~A}$, the controlled slope is $\mathrm{t}_{\mathrm{f}}=\mathrm{t}_{\mathrm{r}}=12 \mathrm{~V} \times \frac{470 \mathrm{pF}}{40 \mu \mathrm{~A}} \times 141 \mu \mathrm{~s}$

### 3.5 Pin 5 - Attenuation

Capacitor $\mathrm{C}_{4}$ connected to pin 5 damps oscillation tendencies.

### 3.6 Pin 6 - Oscillator

The oscillator determines the frequency of the output voltage. This is defined by an external capacitor, $\mathrm{C}_{2}$. It is charged with a constant current, I, until the upper switching threshold is reached. A second current source is then activated which taps a double current, $2 \times I$, from the charging current. The capacitor, $\mathrm{C}_{2}$, is thus discharged by the current, I, until the lower switching threshold is reached. The second source is then switched off again and the procedure starts once more.

### 3.6.1 Example for Oscillator Frequency Calculation

$\mathrm{V}_{\mathrm{T} 100}=\mathrm{V}_{\mathrm{S}} \times \alpha_{1}=\left(\mathrm{V}_{\text {Batt }}-\mathrm{I}_{\mathrm{S}} \times \mathrm{R}_{3}\right) \times \alpha_{1}$
$\mathrm{V}_{\mathrm{T}<100}=\mathrm{V}_{\mathrm{S}} \times \alpha_{2}=\left(\mathrm{V}_{\text {Batt }}-\mathrm{I}_{\mathrm{S}} \times \mathrm{R}_{3}\right) \times \alpha_{2}$
$\mathrm{V}_{\mathrm{TL}}=\mathrm{V}_{\mathrm{S}} \times \alpha_{3}=\left(\mathrm{V}_{\text {Batt }}-\mathrm{I}_{\mathrm{S}} \times \mathrm{R}_{3}\right) \times \alpha_{3}$
where
$\mathrm{V}_{\mathrm{T} 100}=$ High switching threshold $100 \%$ duty cycle
$\mathrm{V}_{\mathrm{T}<100}=$ High switching threshold $<100 \%$ duty cycle
$\mathrm{V}_{\mathrm{TL}}=$ Low switching threshold
$\alpha_{1}, \alpha_{2}$ and $\alpha_{3}$ are fixed values
The above mentioned threshold voltages are calculated for the following values given in the datasheet.
$V_{\text {Batt }}=12 \mathrm{~V}, \mathrm{I}_{\mathrm{S}}=4 \mathrm{~mA}, \mathrm{R}_{3}=150 \Omega$
$\alpha_{1}=0.7, \alpha_{2}=0.67$ and $\alpha_{3}=0.28$
$\mathrm{V}_{\mathrm{T} 100}=(12 \mathrm{~V}-4 \mathrm{~mA} \times 150 \Omega) \times 0.7 \approx 8 \mathrm{~V}$
$\mathrm{V}_{\mathrm{T}<100}=11.4 \mathrm{~V} \times 0.67=7.6 \mathrm{~V}$
$\mathrm{V}_{\mathrm{TL}}=11.4 \mathrm{~V} \times 0.28=3.2 \mathrm{~V}$
For a duty cycle of $100 \%$, the oscillator frequency, f , is as follows:
$f=\frac{I_{\text {osc }}}{2 \times\left(V_{T 100}-V_{T L}\right) \times C_{2}}$ where $C_{2}=22 n F$ and $I_{\mathrm{osc}}=40 \mu \mathrm{~A}$

Therefore:

$$
\mathrm{f}=\frac{40 \mu \mathrm{~A}}{2 \times(8 \mathrm{~V}-3.2 \mathrm{~V}) \times 22 \mathrm{nF}}=189 \mathrm{~Hz}
$$

For a duty cycle of less than $100 \%$, the oscillator frequency, $f$, is as follows:

$$
\mathrm{f}=\frac{\mathrm{I}_{\mathrm{osc}}}{2 \times\left(\mathrm{V}_{\mathrm{T}<100}-\mathrm{V}_{\mathrm{TL}}\right) \times \mathrm{C}_{2}+4 \times \mathrm{V}_{\text {Batt }} \times \mathrm{C}_{4}}
$$

where $\mathrm{C}_{4}=470 \mathrm{pF}$

$$
\mathrm{f}=\frac{40 \mu \mathrm{~A}}{2 \times(7.6 \mathrm{~V}-3.2 \mathrm{~V}) \times 22 \mathrm{nF}+4 \times 12 \mathrm{~V} \times 470 \mathrm{pF}}=185 \mathrm{~Hz}
$$

A selection of different values of $\mathrm{C}_{2}$ and $\mathrm{C}_{4}$ provides a range of oscillator frequencies from 10 Hz to 2000 Hz .

### 3.7 Pins 7, 8, 10 and 15

Not connected.

### 3.8 Pin 9 - Status Short Circuit Latch

The status of the short-circuit latch can be monitored via pin 9 (open collector output).
Table 3-2. Pin 9 Function

| Pin 9 | Function |
| :--- | :--- |
| L | Short-circuit detected |
| H | No short-circuit detected |

### 3.9 Pins 11 and 12 - Short-circuit Protection and Current Sensing

### 3.9.1 Short-circuit Detection and Time Delay $\mathrm{t}_{\mathrm{d}}$

The lamp current is monitored by means of an external shunt resistor. If the lamp current exceeds the threshold for the short-circuit detection circuit ( $\mathrm{V}_{\mathrm{T} 2} \approx 90 \mathrm{mV}$ ), the duty cycle is switched over to $100 \%$ and capacitor $\mathrm{C}_{5}$ is charged by a current source of $20 \mu \mathrm{~A}\left(\mathrm{I}_{\mathrm{ch}}-I_{\text {dis }}\right)$. The external FET is switched off after the cut-off threshold $\left(\mathrm{V}_{\mathrm{T} 11}\right)$ is reached. Renewed switching on of the FET is possible only after a power-on reset. The current source, $\mathrm{I}_{\text {dis }}$, ensures that capacitor $\mathrm{C}_{5}$ is not charged by parasitic currents. Capacitor $\mathrm{C}_{5}$ is discharged by $\mathrm{I}_{\text {dis }}$ to typically 0.7 V .

Time delay, $t_{d}$, is as follows:
$\mathrm{t}_{\mathrm{d}}=\mathrm{C}_{5} \times \frac{\left(\mathrm{V}_{11}-0.7 \mathrm{~V}\right)}{\left(\mathrm{I}_{\mathrm{ch}}-\mathrm{I}_{\text {dis }}\right)}$
With $\mathrm{C}_{5}=330 \mathrm{nF}$ and $\mathrm{V}_{\text {Batt }}=12 \mathrm{~V}$, we have
$\mathrm{t}_{\mathrm{d}}=330 \mathrm{nF} \times \frac{(9.8 \mathrm{~V}-0.7 \mathrm{~V})}{20 \mu \mathrm{~A}}=150 \mathrm{~ms}$

### 3.9.2 Current Limitation

The lamp current is limited by a control amplifier that protects the external power transistor. The voltage drop across an external shunt resistor acts as the measured variable. Current limitation takes place for a voltage drop of $\mathrm{V}_{\mathrm{T} 1} \approx 100 \mathrm{mV}$. Owing to the difference $\mathrm{V}_{\mathrm{T}}-\mathrm{V}_{\mathrm{T} 2} \approx 10 \mathrm{mV}$, current limitation occurs only when the short-circuit detection circuit has responded.

After a power-on reset, the output is inactive for half an oscillator cycle. During this time, the supply voltage capacitor can be charged so that current limitation is guaranteed in the event of a short-circuit when the IC is switched on for the first time.

### 3.10 Pins 13 and 14 - Charge Pump and Output

Pin 14 (output) is suitable for controlling a power MOSFET. During the active integration phase, the supply current of the operational amplifier is mainly supplied by capacitor $\mathrm{C}_{3}$ (bootstrapping). Additionally, a trickle charge is generated by an integrated oscillator ( $\mathrm{f}_{13} \approx 400 \mathrm{kHz}$ ) and a voltage doubler circuit. This permits a gate voltage supply at a duty cycle of $100 \%$.

### 3.11 Pin 16 - Supply Voltage, $\mathrm{V}_{\mathrm{s}}$ or $\mathrm{V}_{\text {Batt }}$

### 3.11.1 Undervoltage Detection

In the event of voltages of approximately $\mathrm{V}_{\text {Batt }}<5.0 \mathrm{~V}$, the external FET is switched off and the latch for short-circuit detection is reset.

A hysteresis ensures that the FET is switched on again at approximately $\mathrm{V}_{\text {Batt }} \geq 5.4 \mathrm{~V}$.

### 3.11.2 Overvoltage Detection

- Stage 1
- If overvoltages of $\mathrm{V}_{\text {Batt }}>20 \mathrm{~V}$ (typically) occur, the external transistor is switched off and switched on again at $\mathrm{V}_{\text {Batt }}<18.5 \mathrm{~V}$ (hysteresis).
- Stage 2
- If $\mathrm{V}_{\text {Batt }}>28.5 \mathrm{~V}$ (typically), the voltage limitation of the IC is reduced from 26 V to 20 V . The gate of the external transistor remains at the potential of the IC ground, thus producing voltage sharing between the FET and lamps in the event of overvoltage pulses (for example, load dump). The short-circuit protection is not in operation. At $V_{\text {Batt }}<23 \mathrm{~V}$, the overvoltage detection stage 2 is switched off.


## 4. Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability

| Parameters | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Junction temperature | $\mathrm{T}_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Ambient temperature range | $\mathrm{T}_{\mathrm{amb}}$ | -40 to +110 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature range | $\mathrm{T}_{\text {stg }}$ | -55 to +125 | ${ }^{\circ} \mathrm{C}$ |

## 5. Thermal Resistance

| Parameters | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Junction ambient | $\mathrm{R}_{\mathrm{thJA}}$ | 120 | K/W |

## 6. Electrical Characteristics

$\mathrm{T}_{\mathrm{amb}}=-40$ to $+110^{\circ} \mathrm{C}, \mathrm{V}_{\text {Batt }}=9 \mathrm{~V}$ to 16.5 V (basic function is guaranteed between 6.0 V and 9.0 V ), reference point ground, unless otherwise specified (Figure 1-1 on page 1). All other values refer to pin GND (pin 1).

| Parameters | Test Conditions | Symbol | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Current consumption | Pin 16 | $\mathrm{I}_{\mathrm{S}}$ |  |  | 6.8 | mA |
| Supply voltage | Overvoltage detection, stage 1 | $V_{\text {Batt }}$ |  |  | 25 | V |
| Stabilized voltage | $\mathrm{I}_{\mathrm{S}}=10 \mathrm{~mA}, \mathrm{pin} 16$ | $\mathrm{V}_{\mathrm{S}}$ | 24.5 |  | 27.0 | V |
| Battery undervoltage detection | $\begin{aligned} & \hline- \text { On } \\ & \text { - Off } \end{aligned}$ | $V_{\text {Batt }}$ | $\begin{aligned} & 4.4 \\ & 4.8 \end{aligned}$ | $\begin{aligned} & 5.0 \\ & 5.4 \end{aligned}$ | $\begin{aligned} & 5.6 \\ & 6.0 \end{aligned}$ | V |
| Battery Overvoltage Detection Pin 2 |  |  |  |  |  |  |
| Stage 1: | $\begin{aligned} & \text { - On } \\ & \text { - Off } \end{aligned}$ | $V_{\text {Batt }}$ | $\begin{aligned} & 18.3 \\ & 16.7 \end{aligned}$ | $\begin{aligned} & 20.0 \\ & 18.5 \end{aligned}$ | $\begin{aligned} & 21.7 \\ & 20.3 \end{aligned}$ | V |
| Stage 2: | $\begin{aligned} & \text { - On } \\ & \text { - Off } \end{aligned}$ | $V_{\text {Batt }}$ | $\begin{aligned} & 25.5 \\ & 19.5 \end{aligned}$ | $\begin{aligned} & 28.5 \\ & 23.0 \end{aligned}$ | $\begin{aligned} & 32.5 \\ & 26.5 \end{aligned}$ | V |
| Stabilized voltage | $\mathrm{I}_{\mathrm{S}}=30 \mathrm{~mA}, \mathrm{pin} 16$ | $\mathrm{V}_{\mathrm{Z}}$ | 18.5 | 20.0 | 21.5 | V |
| Short-circuit Protection Pin 12 |  |  |  |  |  |  |
| Short-circuit current limitation | $\mathrm{V}_{\mathrm{T} 1}=\mathrm{V}_{\mathrm{S}}-\mathrm{V}_{12}$ | $\mathrm{V}_{\mathrm{T} 1}$ | 85 | 100 | 120 | mV |
| Short-circuit detection | $\mathrm{V}_{\mathrm{T} 2}=\mathrm{V}_{\mathrm{S}}-\mathrm{V}_{12}$ | $\mathrm{V}_{\text {T2 }}$ | 75 | 90 | 105 | mV |
|  |  | $\mathrm{V}_{\mathrm{T} 1}-\mathrm{V}_{\mathrm{T} 2}$ | 3 | 10 | 30 | mV |
| Delay Timer Short-circuit Detection | Pin 11 |  |  |  |  |  |
| Switched off threshold | $\mathrm{V}_{\mathrm{T} 11}=\mathrm{V}_{\mathrm{S}}-\mathrm{V}_{11}$ | $\mathrm{V}_{\text {T11 }}$ | 9.5 | 9.8 | 10.1 | V |
| Charge current |  | $\mathrm{I}_{\mathrm{ch}}$ |  | 23 |  | $\mu \mathrm{A}$ |
| Discharge current |  | $\mathrm{I}_{\text {dis }}$ |  | 3 |  | $\mu \mathrm{A}$ |
| Capacitance current | $\mathrm{I}_{5}=\mathrm{I}_{\mathrm{ch}}-\mathrm{I}_{\text {dis }}$ | $\mathrm{I}_{5}$ | 13 | 20 | 27 | mA |
| Output Short-circuit Latch Pin 9 |  |  |  |  |  |  |
| Saturation voltage | $\mathrm{I}_{9}=100 \mu \mathrm{~A}$ | $\mathrm{V}_{\text {sat }}$ |  | 150 | 350 | mV |
| Voltage Doubler | Pin 13 |  |  |  |  |  |
| Voltage | Duty cycle 100\% | $\mathrm{V}_{13}$ | $2 \mathrm{~V}_{\text {S }}$ |  |  |  |
| Oscillator frequency |  | $\mathrm{f}_{13}$ | 280 | 400 | 520 | kHz |

Note: 1. Reference point is battery ground

## 6. Electrical Characteristics (Continued)

$\mathrm{T}_{\text {amb }}=-40$ to $+110^{\circ} \mathrm{C}, \mathrm{V}_{\text {Batt }}=9 \mathrm{~V}$ to 16.5 V (basic function is guaranteed between 6.0 V and 9.0 V ), reference point ground, unless otherwise specified (Figure 1-1 on page 1). All other values refer to pin GND (pin 1).

| Parameters | Test Conditions | Symbol | Min. | Typ. | Max. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Internal voltage limitation | $\mathrm{I}_{13}=5 \mathrm{~mA}$ | $\mathrm{V}_{13}$ | 26 | 27.5 | 30.0 | V |
|  | (Whichever is lower) | $\mathrm{V}_{13}$ | $\left(\mathrm{V}_{\mathrm{S}+14}\right)$ | $\left(\mathrm{V}_{\mathrm{S}+15}\right)$ | $\left(\mathrm{V}_{\mathrm{S}+16}\right)$ |  |
| Gate Output | Pin 14 |  |  |  |  |  |
| Voltage | Low level | $\mathrm{V}_{14}$ | 0.35 | 0.70 | 0.95 | V |
|  | $\begin{aligned} & \mathrm{V}_{\text {Batt }}=16.5 \mathrm{~V}, \mathrm{~T}_{\mathrm{amb}}=110^{\circ} \mathrm{C}, \\ & \mathrm{R}_{3}=150 \Omega \end{aligned}$ |  |  |  | $1.5{ }^{(1)}$ |  |
|  | High level, duty cycle 100\% | $\mathrm{V}_{14}$ |  | $\mathrm{V}_{13}$ |  |  |
| Current | $\mathrm{V}_{14}$ = Low level | $\mathrm{I}_{14}$ | 1.0 |  |  | mA |
|  | $\mathrm{V}_{14}=$ High level, $\mathrm{I}_{13}>\left\|\mathrm{I}_{14}\right\|$ |  | -1.0 |  |  |  |
| Enable/Disable Pin 2 | Pin 2 |  |  |  |  |  |
| Current | $\mathrm{V}_{2}=0 \mathrm{~V}$ | $\mathrm{I}_{2}$ | -20 | -40 | -60 | $\mu \mathrm{A}$ |
| Duty Cycle Reduction Pin 4 | Pin 4 |  |  |  |  |  |
| Z-voltage | $\mathrm{I}_{4}=500 \mu \mathrm{~A}$ | $\mathrm{V}_{4}$ | 6.9 | 7.4 | 8.0 | V |
| Oscillator |  |  |  |  |  |  |
| Frequency | Pin 6 | f | 10 |  | 2000 | Hz |
| Threshold cycle Upper | $\mathrm{V}_{14}=$ High, $\alpha_{1}=\frac{\mathrm{V}_{\mathrm{T} 100}}{\mathrm{~V}_{\mathrm{S}}}$ | $\alpha_{1}$ | 0.68 | 0.7 | 0.72 |  |
| Lower | $\mathrm{V}_{14}=$ Low, $\alpha_{2}=\frac{\mathrm{V}_{\mathrm{T}<100}}{\mathrm{~V}_{\mathrm{S}}}$ | $\alpha_{2}$ | 0.65 | 0.67 | 0.69 |  |
|  | $\alpha_{3}=\frac{V_{T L}}{V_{S}}$ | $\alpha_{3}$ | 0.26 | 0.28 | 0.3 |  |
| Oscillator current | $\mathrm{V}_{\text {Batt }}=1 \mathrm{~V}$ | $\pm \mathrm{l}_{\text {osc }}$ | 26 | 40 | 54 | $\mu \mathrm{A}$ |
| Frequency tolerance | $\begin{aligned} & \mathrm{C}_{4} \text { open, } \mathrm{C}_{2}=470 \mathrm{nF}, \\ & \text { duty cycle }=50 \% \end{aligned}$ | f | 6.0 | 9.9 | 13.5 | Hz |

Note: 1. Reference point is battery ground

## 7. Application

Figure 7-1. Application

8. Ordering Information

| Extended Type Number | Package | Remarks |
| :--- | :--- | :--- |
| U6084B-MFPG3Y | SO16 | Taped and reeled, Pb-free |

9. Package Information SO16

10. Revision History

Please note that the following page numbers referred to in this section refer to the specific revision mentioned, not to this document.

| Revision No. | History |
| :--- | :--- |
| 4677B-AUTO-02/03 | - Block Diagram on Page 1 changed <br> - New heading rows at Table "Absolute Maximum Ratings" on page 6 <br> added |
|  | - Put datasheet in a new template <br> - Updated text to new style guide |
| 4677C-AUTO-08/05 | - First page: Pb-free logo added <br> - Page 9: Ordering Information changed |

## Atmel Corporation

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 487-2600

## Regional Headquarters

Europe<br>Atmel Sarl<br>Route des Arsenaux 41<br>Case Postale 80<br>$\mathrm{CH}-1705$ Fribourg<br>Switzerland<br>Tel: (41) 26-426-5555<br>Fax: (41) 26-426-5500

Asia
Room 1219
Chinachem Golden Plaza
77 Mody Road Tsimshatsui
East Kowloon
Hong Kong
Tel: (852) 2721-9778
Fax: (852) 2722-1369
Japan
9F, Tonetsu Shinkawa Bldg.
1-24-8 Shinkawa
Chuo-ku, Tokyo 104-0033
Japan
Tel: (81) 3-3523-3551
Fax: (81) 3-3523-7581

## Atmel Operations

Memory<br>2325 Orchard Parkway<br>San Jose, CA 95131, USA<br>Tel: 1(408) 441-0311<br>Fax: 1(408) 436-4314

## Microcontrollers

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 436-4314
La Chantrerie
BP 70602
44306 Nantes Cedex 3, France
Tel: (33) 2-40-18-18-18
Fax: (33) 2-40-18-19-60
ASIC/ASSP/Smart Cards
Zone Industrielle
13106 Rousset Cedex, France
Tel: (33) 4-42-53-60-00
Fax: (33) 4-42-53-60-01
1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906, USA
Tel: 1(719) 576-3300
Fax: 1(719) 540-1759
Scottish Enterprise Technology Park
Maxwell Building
East Kilbride G75 0QR, Scotland
Tel: (44) 1355-803-000
Fax: (44) 1355-242-743

## RF/Automotive

Theresienstrasse 2
Postfach 3535
74025 Heilbronn, Germany
Tel: (49) 71-31-67-0
Fax: (49) 71-31-67-2340
1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906, USA
Tel: 1(719) 576-3300
Fax: 1(719) 540-1759
Biometrics/Imaging/Hi-Rel MPU/
High Speed Converters/RF Datacom
Avenue de Rochepleine
BP 123
38521 Saint-Egreve Cedex, France
Tel: (33) 4-76-58-30-00
Fax: (33) 4-76-58-34-80

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